Please add new claims 19-22 as follows:
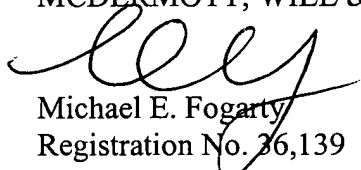
--19. The data processor of Claim 12,

wherein said memory network has a bus network structure.

20. The data processor of Claim 12,

wherein said memory network has a ring network structure.

21. The data processing method of Claim 10 or 13,

wherein said memory network has a bus network structure.

22. The data processing method of Claim 10 or 13,

wherein said memory network has a ring network structure.--

REMARKS

Applicants request entry of the foregoing amendments prior to examination of the above-identified application. No new matter has been added.

Respectfully submitted,

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